

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2	"6584037"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 09:04
S2	1	("6275556").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/12/08 09:05
S3	1	("6016080").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/12/08 09:06
S4	1	("5859986").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/12/08 09:07
S5	1	("5734685").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/12/08 09:08
S6	1	("5522088").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/12/08 09:08
S7	0	(defin\$3 near2 (clock adj1 pulse adj1 period)) same (correction adj1 clock adj1 pulse) same samp1\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 09:10
S8	1	(defin\$3 near2 (clock adj1 pulse adj1 period)) same (correction adj1 clock adj1 pulse)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 09:12
S9	1	(defin\$3 near2 (clock adj1 pulse adj1 period)) same (correction adj1 clock\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 09:12
S10	0	(defin\$3 near2 (clock adj1 pulse adj1 period)) and oversamp1\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 09:13
S11	10	((clock adj1 pulse adj1 period)) and oversamp1\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 09:16

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S12	0	(reference near2 clock near2 signal near2 period) and (two adj1 data adj1 stream\$1) and (pll or (phase adj1 lock\$2 adj1 loop))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 09:20
S13	35	(reference near2 clock near2 signal near2 period) and (data adj1 stream\$1) and (pll or (phase adj1 lock\$2 adj1 loop))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 09:29
S14	80	(clock near3 recover\$3) and (clock adj1 period) and ((pll or (phase adj1 locked adj1 loop)) with (reference adj1 clock adj1 signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 09:30
S15	49	(clock near3 recover\$3) and (clock adj1 period) and ((pll or (phase adj1 locked adj1 loop)) with (reference adj1 clock adj1 signal)) and (vco or (voltage adj1 controlled adj1 oscilator))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 09:30
S16	0	(clock near3 recover\$3) and (clock adj1 period) and ((pll or (phase adj1 locked adj1 loop)) with (reference adj1 clock adj1 signal)) and (vco or (voltage adj1 controlled adj1 oscilator)) and data1 and data2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 09:31
S17	3	(clock near3 recover\$3) and ((pll or (phase adj1 locked adj1 loop)) with (reference adj1 clock adj1 signal)) and (vco or (voltage adj1 controlled adj1 oscilator)) and data1 and data2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 09:36
S18	0	(clock near3 recover\$3) and ((pll or (phase adj1 locked adj1 loop)) with (reference adj1 clock adj1 signal)) and (vco or (voltage adj1 controlled adj1 oscilator)) and ((data adj1 stream\$1) with (overlap\$3 near3 time adj1 period\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 09:39
S19	0	(clock near3 recover\$3) and ((pll or (phase adj1 locked adj1 loop)) with (reference adj1 clock adj1 signal)) and (vco or (voltage adj1 controlled adj1 oscilator)) and ((data adj1 stream\$1) with (overlap\$3 near3 period\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 09:39

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S20	0	(clock near3 recover\$3) and ((pll or (phase adj1 locked adj1 loop)) with (reference adj1 clock adj1 signal)) and (vco or (voltage adj1 controlled adj1 oscilator)) and ((data adj1 stream\$1) with (overlap\$4 near3 period\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 09:40
S21	1	(clock near3 recover\$3) and ((pll or (phase adj1 locked adj1 loop)) with (reference adj1 clock adj1 signal)) and (vco or (voltage adj1 controlled adj1 oscilator)) and ((data adj1 stream\$1) with (overlap\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 10:04
S22	0	(clock near3 recover\$3) and ((pll or (phase adj1 locked adj1 loop)) with (reference adj1 clock adj1 signal)) and (vco or (voltage adj1 controlled adj1 oscilator)) and ((data adj1 bit\$1) with (overlap\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 09:43
S23	0	((pll or (phase adj1 locked adj1 loop)) with (reference adj1 clock adj1 signal)) and (vco or (voltage adj1 controlled adj1 oscilator)) and ((data adj1 bit\$1) with (overlap\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 09:43
S24	8	((pll or (phase adj1 locked adj1 loop))) and (vco or (voltage adj1 controlled adj1 oscilator)) and ((data adj1 bit\$1) with (overlap\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 09:44
S25	93	(clock near3 recover\$3) and ((pll or (phase adj1 locked adj1 loop)) with (reference adj1 clock adj1 signal)) and (vco or (voltage adj1 controlled adj1 oscilator)) and (data adj1 (stream\$1 or bit\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 10:08
S26	76	(clock near3 recover\$3) and ((pll or (phase adj1 locked adj1 loop)) with (reference adj1 clock adj1 signal)) and (vco or (voltage adj1 controlled adj1 oscilator)) and (data adj1 (stream\$1 or bit\$1)) and (phase adj1 detect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 12:53
S27	1	("20010006538").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/12/08 13:06
S28	274	((phase adj1 locked adj2 loop) with (reference adj1 clock)) same (phase adj1 detector)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 13:08

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S29	138	((phase adj1 locked adj2 loop) with (reference adj1 clock)) same (phase adj1 detector) same (vco or (voltage adj1 controlled adj1 oscillator))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 13:09
S30	31	((phase adj1 locked adj2 loop) near3 generat\$3 same (reference adj1 clock)) same (phase adj1 detector) same (vco or (voltage adj1 controlled adj1 oscillator))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 13:14
S31	366	(master with slave with (pll or (phase adj1 locked adj1 loop)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 13:14
S32	46	(master with slave with (pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) with (vco or (voltage adj1 controlled adj1 oscillator))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 13:29
S33	455	(first adj1 (pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) with (vco or (voltage adj1 controlled adj1 oscillator))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 13:29
S34	62	(first adj1 (pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) with (vco or (voltage adj1 controlled adj1 oscillator)) and (data adj1 (stream\$1 or bit\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 13:30
S35	6	(first adj1 (pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) with (vco or (voltage adj1 controlled adj1 oscillator)) and (sampl\$3 near4 (data adj1 (stream\$1 or bit\$1)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 13:36
S36	0	(first adj1 (pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) with (vco or (voltage adj1 controlled adj1 oscillator)) and (sampl\$3 near4 (data adj1 (stream\$1 or bit\$1))) and (overlap\$3 near4 bit\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 13:37
S37	0	(first adj1 (pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) with (vco or (voltage adj1 controlled adj1 oscillator)) and ((data adj1 (stream\$1 or bit\$1))) and (overlap\$3 near4 bit\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 13:37

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S38	0	(first adj1 (pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) with (vco or (voltage adj1 controlled adj1 oscillator)) and (overlap\$3 near4 bit\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 13:38
S39	100	(first adj1 (pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) with (vco or (voltage adj1 controlled adj1 oscillator)) and (overlap\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 13:38
S40	7	(first adj1 (pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) with (vco or (voltage adj1 controlled adj1 oscillator)) and (overlap\$3 with select\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 13:39
S41	0	(first adj1 (pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) with (vco or (voltage adj1 controlled adj1 oscillator)) and (clock\$3 with select\$3) and (data1 and data2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 13:40
S42	3	(first adj1 (pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) with (vco or (voltage adj1 controlled adj1 oscillator)) and (clock\$3 with select\$3) and (data1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 13:42
S43	30	(first adj1 (pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) with (vco or (voltage adj1 controlled adj1 oscillator)) and (clock\$3 with select\$3) and jitter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 13:42
S44	37	((first or master) adj1 (pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) with (vco or (voltage adj1 controlled adj1 oscillator)) and (clock\$3 with select\$3) and jitter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 13:53
S45	0	((first or master) adj1 (pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) same (vco or (voltage adj1 controlled adj1 oscillator)) same (de\$1mux or de\$1multiplex\$3) same select\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 13:54
S46	12	((first or master) adj1 (pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) same (vco or (voltage adj1 controlled adj1 oscillator)) same (de\$1mux or de\$1multiplex\$3 or select\$3) same sampI\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 14:47

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S48	86039	((first or master) adj1 (pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) same (vco or (voltage adj1 control\$3 adj1 oscillator)) nad (parallel near2 data)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 14:49
S49	28	((first or master) adj1 (pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) same (vco or (voltage adj1 control\$3 adj1 oscillator)) and (parallel near2 data)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 14:52
S50	16	((first or master) adj1 (pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) same (vco or (voltage adj1 control\$3 adj1 oscillator)) and (parallel near2 data) and (clock near3 select\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 14:59
S51	10	((first or master) adj1 (pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) same (vco or (voltage adj1 control\$3 adj1 oscillator)) and (parallel near2 data) and (clock near3 select\$3) and jitter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 15:01
S52	16	((first or master) adj1 (pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) same (vco or (voltage adj1 control\$3 adj1 oscillator)) and (parallel near2 data) and jitter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 15:06
S53	258	((pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) same (vco or (voltage adj1 control\$3 adj1 oscillator)) and (parallel near2 data) and jitter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 15:07
S54	62	((pll or (phase adj1 locked adj1 loop))) and (phase adj1 detect\$3) same (vco or (voltage adj1 control\$3 adj1 oscillator)) and (parallel near2 data) and jitter and ((select\$3 near3 clock) or (optim\$2 near2 clock))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/08 15:08
S55	3	(reference near2 clock) with (over adj1 sampl\$3) same (select\$3 with clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/11 14:06
S56	10	(reference near2 clock) same (over adj1 sampl\$3) same (select\$3 with clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/11 14:06

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S57	6153	((pll or (phase adj1 lock\$2 adj2 loop\$1)) with (reference near2 clock))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/11 14:22
S58	764	((pll or (phase adj1 lock\$2 adj2 loop\$1)) near3 generat\$3 near3 (reference near2 clock))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/11 14:22
S59	92	S58 and (vco or voltage adj1 control\$3 adj1 oscillator) same (phase adj1 detect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/11 14:23
S60	36	S59 and ((plurality or multiple or multi\$1phase) near2 clock\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/11 14:41
S61	12	S60 and (clock near3 period)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/11 14:59
S62	18	S60 and parallel	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/11 15:17
S63	3	(parallel) and (correlat\$3 near6 clock near6 two near3 bit\$1) and (clock or data) near2 recover\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/11 15:26
S64	7	(parallel) and (correlat\$3 near6 clock near6 two near3 bit\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/11 15:27
S65	30	(parallel) and (correlat\$3 near6 clock near6 data near3 bit\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/11 15:27
S66	0	(parallel) and (correlat\$3 near6 clock near6 data near3 bit\$1 with overlap\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/11 15:27

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S67	30	(parallel) and (correlat\$3 near6 clock near6 data near3 bit\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/11 15:30
S68	13	(parallel) and (clock near6 overlap\$3 near4 data near3 bit\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/11 15:34
S69	63	(select\$3 near2 clock) with (parallel near2 bit\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/11 15:47
S70	8483	(correlat\$4 with clock with bit\$1 with parallel)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/11 15:48
S71	59	(correlat\$4 with clock with bit\$1 with parallel)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/11 16:14
S72	3	(select\$3 near3 clock\$1) with (jitter near2 correct\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/11 16:15
S73	85	(select\$3 near3 clock\$1) with (jitter) and (parallel)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/11 16:16
S74	37	(select\$3 near3 clock\$1) with (jitter) and (parallel near2 (bit\$1 or stream\$1 or data))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/11 17:13
S75	10	(select\$3 near3 clock\$1) with (jitter) and (parallel near2 (bit\$1 or stream\$1 or data)) and over\$1lapp\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/11 17:14
S76	2111208	(parallel near2 (stream\$1 or data)) with (select\$3 near3 clock\$1) same (overlap\$4 or jitter) smae (latch\$3 or samp\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/12 08:50

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S77	2	(parallel near2 (stream\$1 or data)) with (select\$3 near3 clock\$1) same (overlap\$4 or jitter) same (latch\$3 or sampl\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/12 08:54
S78	3	(parallel near2 (stream\$1 or data)) same ((select\$3 or choos\$3 or determin\$3) near3 clock\$1) same (overlap\$4 or jitter) same (latch\$3 or sampl\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/12 08:57
S79	75	(parallel near2 (stream\$1 or data)) and ((select\$3 or choos\$3 or determin\$3) near3 clock\$1) same (overlap\$4 or jitter) same (latch\$3 or sampl\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/12 08:57
S80	29	(parallel near2 (stream\$1 or data)) and ((select\$3 or choos\$3 or determin\$3) near3 clock\$1) with (overlap\$4 or jitter) same (latch\$3 or sampl\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/12 09:33
S81	28	(parallel near2 (stream\$1 or data)) and ((select\$3 or choos\$3 or determin\$3) near3 clock\$1) with (overlap\$4 or jitter) and (clock near3 sampl\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/12 09:41
S82	0	(parallel near2 (stream\$1 or data)) with ((select\$3 or choos\$3 or determin\$3) near3 clock\$1) with (overlap\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/12 09:42
S83	0	(parallel near2 (stream\$1 or data)) same ((select\$3 or choos\$3 or determin\$3) near3 clock\$1) with (overlap\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/12 13:16
S84	3	(parallel near2 (stream\$1 or data)) same ((select\$3 or choos\$3 or determin\$3) near3 clock\$1) with (jitter)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/12 09:44
S85	6	((select\$3 or choos\$3 or determin\$3) near3 clock\$1) with (overlap\$4 near3 (period or window)) and jitter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/12 09:47
S86	1	((select\$3 or choos\$3 or determin\$3) near3 clock\$1) with (overlap\$4 near3 (period or window) near4 (bit\$1 or stream\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/12 09:48

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S87	7	((select\$3 or choos\$3 or determin\$3) near3 clock\$1) with (overlap\$4 near5 (bit\$1 or stream\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/12 09:49
S88	13	((select\$3 or choos\$3 or determin\$3) near3 clock\$1) with (jitter\$2 near5 (bit\$1 or stream\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/12 09:49
S89	18	(parallel near2 (stream\$1 or data)) same ((select\$3 or choos\$3 or determin\$3) near3 clock\$1 with sampI\$3) and jitter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/12 11:10
S90	17	(correlat\$3 with clock with (data near2 bit\$1) with parallel)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/12 13:16
S91	1573	(parallel near2 (stream\$1 or data or bit\$1)) and ((select\$3 or choos\$3 or determin\$3) near4 clock\$1) and jitter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/12 13:17
S92	200	(parallel near2 (stream\$1 or data or bit\$1)) and ((select\$3 or choos\$3 or determin\$3) near4 clock\$1 near5 sampI\$3) and jitter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/12 13:17
S93	29	(parallel near2 (stream\$1 or data or bit\$1)) and ((select\$3 or choos\$3 or determin\$3) near4 clock\$1 near5 sampI\$3) same jitter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/12/12 13:17